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Institute for form 1449A/PTO				<b>Complete if Known</b>	
				Application Number	10/015,530
				Filing Date	December 12, 2001
				First Named Inventor	Robert T. Plunkett
				Art Unit	2183
				Examiner Name	Aimee Li
Sheet	1	of	2	Attorney Docket Number	046301/046000 (QST-046 US)

**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number – Kind Code <sup>2</sup> (if known)				
	1.	US 4,380,046		04-12-1983	Fung	
	2.	US 4,758,985		07-19-1988	Carter	
	3.	US 4,870,302		09-26-1989	Freeman	
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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				Application Number	10/015,530
Sheet	2	of	2	Filing Date	December 12, 2001
				First Named Inventor	Robert T. Plunkett
				Art Unit	2183
				Examiner Name	Aimee Li
				Attorney Docket Number	046301/046000 (QST-046 US)

**U.S. PUBLISHED PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number – Kind Code <sup>2</sup> (if known)				
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		Country Code <sup>3</sup>	Kind Code <sup>5</sup> (if known)				
44.	WO 00/69073		A1	11-16-2000	Wasson		

**OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	45.	Altera Apex 20K 1999	
	46.	Andraka Consulting Group, "Distributed Arithmetic," Obtained from: <a href="http://www.fpga-guru.com/distribu.htm">http://www.fpga-guru.com/distribu.htm</a> (1998-2000)	
	47.	Hanna et al., "A Normalized Backpropagation Learning Algorithm for Multilayer Feed-Forward Neural Adaptive Filters," <i>Neural Networks for Signal Processing XI</i> , Proceedings of the 2001 IEEE Signal Processing Society Workshop pp. 63-72 (Sept 2001)	
	48.	Janweijer et al., "A Compact Robin Using the SHarc (CRUSH)," Obtained from: <a href="http://www.nikhef.nl/~peterj/Crush/CRUSH-hw.pdf">http://www.nikhef.nl/~peterj/Crush/CRUSH-hw.pdf</a> (Sept 1998)	
	49.	Rajagopalan et al., "A Flexible Multiplication Unit for an FPGA Logic Block," <i>Circuits and Systems</i> 4:546-9 (2001)	
	50.	Xilinx Data Book 1998	
	51.	Xilinx Virtex 1.1 1998	
	52.	Xilinx Virtex 2.2 2000	

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